

L1_Cal Int/Comm Team Toolbox

October 3 – October 7, 2005

Mon, Oct. 3 Cory Lyn Mike Nayeem Sabine	ADF→tape: Scheduling physics run to test TAB global firmware and elimination of crazy 8's(bent pin). Jaro investigating eta # scheme. Algo/Sim: Jaro submitted new SW firmware. Calculations starting too early caused truncation—overflow problems. Testing today. ADF→L2:Evidence of SCL inits causing errors. Investigating. ALL:Define tasks for milestone.
Tues, Oct.4 Alan Cory Hal J. Green Lyn Mike Nikos Nayeem Sabine	ADF→tape: Zbias run planned today. Algo/Sim: Chip 8 correction submitted by Jaro. Testing it today. TRANS_SYS: ATC leads need trimmed. ATC panel machining done. 47 ATC assembly started yesterday. 30/75 PP boards assembled. Shipping today or tomorrow GAB→TFW/L2/L3: Establishing L1_Cal_Ilb trig term. INFRA:Rack layout defined, EPICS PS spreadsheet linked, naming conventions discussed.
Wed, Oct. 5 Cory Jorge Lyn Mike Nayeem Sabine	ADF→tape: Zbias run successful w/canned ADF data. We will rotate the task of weekly 0bias runs among us: Cory is 1 st . **VRB in slot 9 is programmed for data runs—App:D0_trig. Slot 11 is App_6 only used for testing. Algo/Sim: Jet algo and sim agree w/Cory code!! YEAH! No trunc/overflow errors. Cory planning a 1 hr pseudo random run today. TRANS_SYS: ATC leads trimmed on 18 units. Ordered new FP screws. GAB→TFW/L2/L3: Adjusting timing. INFRA:Task list submitted to EE support. Cal_Trk: Need timing firmware mod reinstalled in global. TAB-->L2:Testing new splitters.
Thurs, Oct. 6 Cory Lyn	Cal_Track: Measured latency through system (BC10 trig/canned ADF data to Cal_Track o/p)=5.5 ticks Algo/Sim:Pseudo random run ok. Sum calc from global understood-signal processing includes rounding, verifying correctness of method. INFRA:Elect. group agreed to provide support between operations activities. TAB/GAB to L2/L3 fiber plant defined.
Fri, Oct. 7 Cory Lyn Mike	Operations meeting: Nayeem Algo/Sim: 1k evts pseudo random run ok. Sum calc method in global verifying—ok. ADF→tape: Prec vs RunIlb run planned. GAB→TFW/L2/L3:Timing in.